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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

In re the Application of

Attn: OIPE

Akihiko EBINA et al.

Application No.: 10/052,255

Docket No.: 111778

FILE COPY

Filed: January 23, 2002

For: METHOD OF MANUFACTURING SEMICONDUCTOR INTEGRATED CIRCUIT
DEVICE INCLUDING NONVOLATILE SEMICONDUCTOR MEMORY
DEVICES

REQUEST FOR CORRECTION OF PALM RECORDS

Director of the U.S. Patent and Trademark Office
Washington, D.C. 20231

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Respectfully submitted,

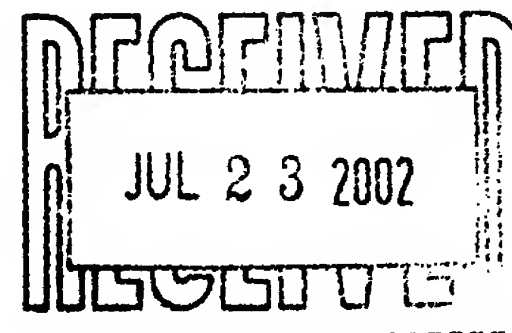
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JAO:TJP/mlb

Date: June 6, 2002

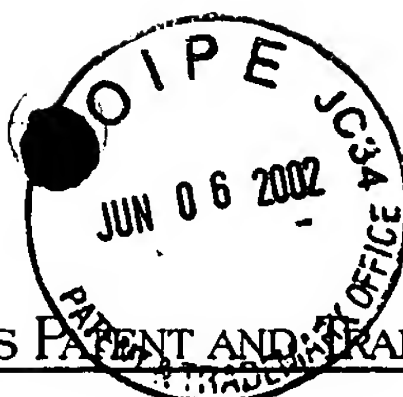
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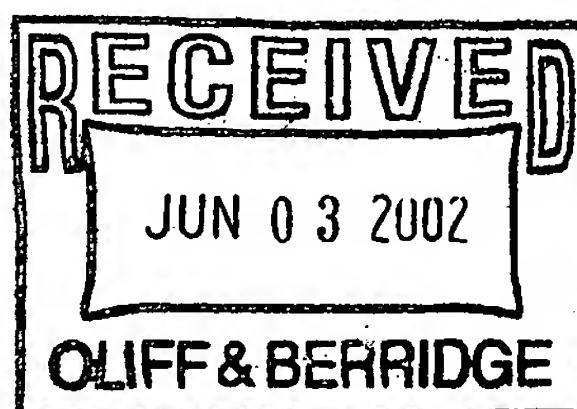
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/052,255	01/23/2002	2812	870	111778	11	15	1

25944
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CONFIRMATION NO. 1930

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OC000000008212686

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Assignment For Published Patent Application

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Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2001-021931(P) 01/30/2001

If Required, Foreign Filing License Granted 02/19/2002

Projected Publication Date: 09/12/2002

Non-Publication Request: No

Early Publication Request: No

Title

Method of manufacturing semiconductor integrated circuit device including nonvolatile

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semiconductor memory devices

Preliminary Class

438

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